

Andino XIO Base – Datasheet

	Raspberry Pi 4:	Raspberry CM4 with Andino CM4 Adapter	
SoC	Broadcom BCM2711, Quad core Cortex-A72 (ARM v8) 64-bit SoC @ 1.5GHz	Broadcom BCM2837B0, Cortex-A53 (ARMv8) 64-bit SoC @ 1.4GHz	
RAM	4GB LPDDR4-3200 SDRAM	4GB LPDDR2 SDRAM	
Flash	None	32GB eMMC Flash	
PCIe	None	Interla PCIe X1 Port. Can be used for M.2 SSD via Adapter	
WiFi	2.4 GHz and 5.0 GHz IEEE 802.11ac wireless	None	
Bluetooth	Bluetooth 5.0, BLE	None	
Ethernet	Gigabit Ethernet	Gigabit Ethernet over USB 2.0 (max. throughput 300 Mbps)	
Connectivity	2 USB 3.0 ports; 2 USB 2.0 ports. Raspberry Pi standard 40 pin GPIO header 2 × micro-HDMI ports (up to 4kp60) Micro-SD card slot (accessible from outside Andino housing)	2 USB 2.0 ports external 1USB 2.0 port internal Full-size HDMI Extended 40-pin GPIO header	
Delivery	Andino IO, RaspberryPi 4, Breadboard, DIN rail Housing	Andino IO, CM4, Breadboard, DIN rail Housing	
all variants			
Power Supply	Wide range DC input 9-24V, Out: 5V, 2.6A	Housing variant	Top-hat rail housing (Plastic)
I/O's	6 galvanic Isolation Inputs (isolated up to 5kV) 6 Relay Outputs (max. 240 VAC, 16 A)	RTC	Integrated, battery-buffered Real Time Clock, DS3231 Dallas Semiconductors Accuracy: ± 2ppm between 0 °C and +40 °C
EMC	DIN EN 61000-6-2/3	Expander	I2C (option)